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**NNgen**  
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**Shinya Takamaeda-Yamazaki**

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## **CONTENTS:**

<b>1</b>	<b>operator</b>	<b>1</b>
<b>2</b>	<b>pynq</b>	<b>7</b>
<b>3</b>	<b>Indices and tables</b>	<b>9</b>
<b>Index</b>		<b>11</b>



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**CHAPTER  
ONE**

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**OPERATOR**

```
class nngen.operator.basic.add(x, y, dtype=None, name=None, par=1)
```

Returns  $x + y$  element-wise.

**Parameters**

- x – A tensor.
- y – A tensor.
- dtype – Output data type (optional).
- name – A name for the operation (optional).
- par – The number of parallel operations (optional).

```
class nngen.operator.basic.sub(x, y, dtype=None, name=None, par=1)
```

Returns  $x - y$  element-wise.

**Parameters**

- x – A tensor.
- y – A tensor.
- dtype – Output data type (optional).
- name – A name for the operation (optional).
- par – The number of parallel operations (optional).

```
class nngen.operator.basic.add_n(arg, dtype=None, name=None, par=1)
```

Adds all input tensors element-wise.

**Parameters**

- arg – A list of Tensor objects.
- dtype – Output data type (optional).
- name – A name for the operation (optional).
- par – The number of parallel operations (optional).

```
class nngen.operator.basic.lshift(x, y, dtype=None, name=None, par=1)
```

Elementwise computes the bitwise left-shift of x and y.

**Parameters**

- x – A tensor.
- y – A tensor.

- `dtype` – Output data type (optional).
- `name` – A name for the operation (optional).
- `par` – The number of parallel operations (optional).

```
class nngen.operator.basic.rshift(x, y, dtype=None, name=None, par=1)
```

Elementwise computes the bitwise right-shift of `x` and `y`.

#### Parameters

- `x` – A tensor.
- `y` – A tensor.
- `dtype` – Output data type (optional).
- `name` – A name for the operation (optional).
- `par` – The number of parallel operations (optional).

```
class nngen.operator.basic.rshift_round(x, y, dtype=None, name=None, par=1)
```

Elementwise computes the bitwise right-shift of `x` and `y` with rounding.

#### Parameters

- `x` – A tensor.
- `y` – A tensor.
- `dtype` – Output data type (optional).
- `name` – A name for the operation (optional).
- `par` – The number of parallel operations (optional).

```
class nngen.operator.basic.multiply(x, y, dtype=None, name=None, par=1)
```

Returns  $x * y$  element-wise.

#### Parameters

- `x` – A tensor.
- `y` – A tensor.
- `dtype` – Output data type (optional).
- `name` – A name for the operation (optional).
- `par` – The number of parallel operations (optional).

```
class nngen.operator.basic.reduce_sum(input_tensor, axis=None, keep_dims=False, dtype=None, name=None, par=1)
```

Computes the sum of elements across dimensions of a tensor.

#### Parameters

- `input_tensor` – A tensor.
- `axis` – The dimensions to reduce (optional).
- `keep_dims` – If true, retains reduced dimensions with length 1 (optional).
- `dtype` – Output data type (optional).
- `name` – A name for the operation (optional).
- `par` – The number of parallel operations (optional).

---

```
nngen.operator.basic.reshape(tensor, shape, dtype=None, name=None)
```

Reshapes a tensor.

Given tensor, this operation returns a tensor that has the same values as tensor with shape *shape*.

#### Parameters

- *tensor* – A tensor.
- *shape* – A tensor. Defines the shape of the output tensor.
- *dtype* – Output data type (optional).
- *name* – A name for the operation (optional).

```
nngen.operator.basic.expand_dims(input, axis, name=None)
```

Inserts a dimension of 1 into a tensor's shape.

Given a tensor *input*, this operation inserts a dimension of 1 at the dimension index *axis* of *input*'s shape. The dimension index *axis* starts at zero; if you specify a negative number for *axis* it is counted backward from the end.

#### Parameters

*input* – A Tensor.

```
class nngen.operator.basic.transpose(a, perm=None, dtype=None, name=None)
```

Transposes *a*. Permutes the dimensions according to *perm*.

#### Parameters

- *a* – A Tensor.
- *perm* – A permutation of the dimensions of *a*.
- *dtype* – Output data type (optional).
- *name* – A name for the operation (optional).

```
class nngen.operator.relu.relu(features, dtype=None, name=None, par=1)
```

Applies the rectified linear unit function element-wise:

$$\text{ReLU}(x) = \max(0, x)$$

#### Parameters

- *features* – A Tensor.
- *dtype* – Output data type (optional).
- *name* – A name for the operation (optional).
- *par* – The number of parallel operations (optional).

```
class nngen.operator.relu.relu6(features, dtype=None, name=None, par=1)
```

Applies the element-wise function:

$$\text{ReLU6}(x) = \min(\max(0, x), 6)$$

#### Parameters

- *features* – A Tensor.
- *dtype* – Output data type (optional).
- *name* – A name for the operation (optional).

- par – The number of parallel operations (optional).

```
class nngen.operator.conv2d.conv2d(input, filter, strides, bias=None, scale=None, rshift_mul=None,
                                   rshift_sum=None, rshift_out=None, act_func=None, padding='SAME',
                                   asymmetric_clip=False, dtype=None, mul_dtype=None, sum_dtype=None,
                                   name=None, par_ich=1, par_och=1, par_col=1, par_row=1,
                                   concur_och=None, stationary='filter', input_ram_size=None,
                                   filter_ram_size=None, bias_ram_size=None, scale_ram_size=None,
                                   vshamt_mul_ram_size=None, vshamt_sum_ram_size=None,
                                   vshamt_out_ram_size=None, out_ram_size=None,
                                   disable_keep_input=False, input_shape=None, filter_shape=None,
                                   out_shape=None)
```

Computes a 2-D convolution given 4-D input and filter tensors.

#### Parameters

- input – NHWC (batch, height, width, channel)
- filter – OHWI (outchannel, height, width, inchannel)
- stride – NHWC (N and C are always 1)
- bias (optional) – Tensor for bias addition to outputs.
- scale (optional) – Tensor for scaling to outputs.
- rshift\_mul (optional) – Arithmetic shift right can be performed on the result of activation and kernel multiplication as needed. Designation of shift amount can select constant and vector. The constants are register generated and the vector is configured in RAM.
- rshift\_sum (optional) – Arithmetic shift right can be performed on the result of accumulation as needed. Designation of shift amount can select constant and vector. The constants are register generated and the vector is configured in RAM.
- rshift\_out (optional) – Arithmetic shift right can be performed on the output value per channel as needed. Designation of shift amount can select constant and vector. The constants are register generated and the vector is configured in RAM.
- act\_func (optional) – The output value of conv2d can be input to the activation function before writing to memory. The activation function that can be specified is the operator that inherited the element-wise class.
- padding (optional) – A string (Only ‘SAME’ padding is supported). The type of padding algorithm to use. The detailed algorithm conforms to Tensorflow.
- asymmetric\_clip (optional) – If this parameter is set to True, the negative range size in clipping operations is increased by one.
- dtype (optional) – Output data type.
- mul\_dtype (optional) – Data type of register that stores the result of multiplication of activation and kernel parameter.
- sum\_dtype (optional) – Data type of register that stores accumulation result.
- name (optional) – A name for the operation (optional).
- par\_ich (optional) – Specifies the degree of operation parallelism in the input channel direction.
- par\_och (optional) – Specifies the degree of operation parallelism in the output channel direction.
- par\_col (optional) – Specifies the degree of operation parallelism in the column direction.

- par\_row (optional) – Specifies the degree of operation parallelism in the row direction.
- concur\_och (optional) – Specifies how many channels of kernel parameters to read simultaneously. If this value is large enough, the efficiency of burst transfer in writing the output result is maximized. If not specified, it is calculated automatically from the relationship between the number of words of memory for kernel parameters and the number of words of memory for output.
- stationary (optional) – Designate data flow to MAC. In ‘weight’ mode, the weight data supplied to the MAC is fixed, while in ‘act’ mode, the input data is fixed.
- input\_ram\_size (optional) – Specify the word length of the input data RAM. If set to less than the minimum required word length (depends on the input data size), the set value is ignored.
- filter\_ram\_size (optional) – Specifies the word length of the kernel parameter RAM. If set to less than the minimum required word length (depends on the kernel parameter size), the set value is ignored.
- bias\_ram\_size (optional) – Specify the word length of the bias parameter RAM. If set to less than the minimum required word length (depends on bias parameter size), the set value is ignored.
- scale\_ram\_size (optional) – Specify the word length of scaling parameter RAM. If set to less than the minimum required word length (depends on the scaling parameter size), the set value is ignored.
- vshamt\_mul\_ram\_size (optional) – Specifies the word length of the RAM that stores the arithmetic right shift value for the result of activation and multiplication of kernel parameters. The setting value is ignored if it is set to the minimum required word length or less.
- vshamt\_sum\_ram\_size (optional) – Specifies the word length of the RAM that stores the arithmetic right shift value for the accumulation result. The setting value is ignored if it is set to the minimum required word length or less.
- vshamt\_out\_ram\_size (optional) – Specifies the word length of the RAM that stores the arithmetic right shift value for the output result. The setting value is ignored if it is set to the minimum required word length or less.
- out\_ram\_size (optional) – Specifies the word length of the output data RAM. If set to less than the minimum required word length (depends on the output data size), the set value is ignored.
- disable\_keep\_input (optional) – If this parameter is set to True, data will be reread from the main memory even if the required amount of input data is full on the on-chip RAM.

## Notes

Note that the original order of tensorflow’s conv2d is HWIO (height, width, inchannel, outchannel)

collect\_local\_control\_param\_values(*index\_offset=0*)

conv2d has no local\_control\_params, but self.act\_func can have local\_control\_params. So collect\_local\_control\_param\_values() returns those of act\_func.

control\_sequence(*fsm*)

must be implemented in each \_Operator class

copy\_local\_control\_params(*obj, index\_offset=0*)

conv2d has no local\_control\_params, but self.act\_func can have local\_control\_params. So all local\_control\_params objects are assigned to act\_func object.

get\_control\_param\_values()

This method must be implemented in each \_Operator class. For chained \_StreamingOperator, this method of the head operator only is called. If control params of intermediate operators in the chain are required, use get\_local\_control\_param\_values()

get\_required\_rams()

@return 3 tuples of (width, length)

get\_required\_substreams()

@return a tuple of (method\_name, args)

get\_stream\_func()

must be implemented in each \_Operator class

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**CHAPTER**

**TWO**

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**PYNQ**



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CHAPTER  
**THREE**

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## **INDICES AND TABLES**

- genindex
- modindex
- search



# INDEX

## A

add (*class in nngen.operator.basic*), 1  
add\_n (*class in nngen.operator.basic*), 1

## C

collect\_local\_control\_param\_values()  
    (*nngen.operator.conv2d.conv2d method*),  
    5  
control\_sequence()  
    (*nngen.operator.conv2d.conv2d method*), 5  
conv2d (*class in nngen.operator.conv2d*), 4  
copy\_local\_control\_params()  
    (*nngen.operator.conv2d.conv2d method*),  
    5

## E

expand\_dims() (*in module nngen.operator.basic*), 3

## G

get\_control\_param\_values()  
    (*nngen.operator.conv2d.conv2d method*),  
    6  
get\_required\_rams()  
    (*nngen.operator.conv2d.conv2d method*), 6  
get\_required\_substreams()  
    (*nngen.operator.conv2d.conv2d method*),  
    6  
get\_stream\_func()  
    (*nngen.operator.conv2d.conv2d method*), 6

## L

lshift (*class in nngen.operator.basic*), 1

## M

multiply (*class in nngen.operator.basic*), 2

## R

reduce\_sum (*class in nngen.operator.basic*), 2  
relu (*class in nngen.operator.relu*), 3  
relu6 (*class in nngen.operator.relu*), 3  
reshape() (*in module nngen.operator.basic*), 2

rshift (*class in nngen.operator.basic*), 2  
rshift\_round (*class in nngen.operator.basic*), 2

## S

sub (*class in nngen.operator.basic*), 1

## T

transpose (*class in nngen.operator.basic*), 3